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Education

■ Ph.D: SHAHID BEHESHTI UNIVERSITY, Computer Engineering- Computer Systems Architecture, 1388→1391

Research Interests

■ VLSI design for beyond-CMOS emerging technologies ; Approximate Computing; Neuromorphic VLSI Circuits ; VLSI Interconnects

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Professional Experiences

■ , 1402→1403

■ , 1401→1402

■ , 1401→Now

■ , 1400→1401

■ , 1400→1401

■ , 1399→1400

■ , 1398→1399

Books

■ مدارهای VLSI پیشرفته

مریم نوروزی، محمدحسین معیری، کیوان ناوی

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Journal Papers

■ Radiation-Immune Spintronic Binary Synapse and Neuron for Process-in-Memory Architecture
Milad Tanavardi Nasab, Abdolah Amirany, Mohammad Hossein Moaiyeri, Kian Jafari
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■ On the Layout-Oriented Investigation of Power Attack Hardness of Spintronic-Based Logic Circuits
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■ Ultra-efficient fully programmable membership function generator based on independent double-gate FinFET technology
Mohammad Khaleqi Qaleh Jooq , Fereshteh Behbahani, Mohammad Hossein Moaiyeri
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■ An SEU-hardened ternary SRAM design based on efficient ternary C-elements using CNTFET technology
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■ Towards Nonvolatile Spintronic Quaternary Flip-Flop and Register Design
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■ A Flexible and Reliable RRAM-Based In-Memory Computing Architecture For Data-Intensive Applications
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■ A Fast and Light Fingerprint-Matching Model Based on Deep Learning Approaches
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■ Highly reliable bio-inspired spintronic/CNTFET multi-bit per cell nonvolatile memory
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thesis and doctoral thesis

■ Design of approximate computational units with error compensation for hardware-accuracy trade-off in   neural

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Farnaz Sabetzadeh
2023

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Soheila Gharavi Hamedani
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M.Sc. Theses

■ Efficient in-memory computing architectures based on emerging memory elements
Milad Ashtari gargari
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■ Strengthening spintronic based neural network hardware against side-channel attacks
Milad Ghazanfari Nasrabad
2023

■ Design and Optimization of Hardware Associative Memories Based on Emerging Technologies
Mahan Rezaei
2023

■ Design of High-Performance Approximate Spintronic Memories for Hardware Implementation of Neural Networks
Hamid Nasr esfahani
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■ Design of Non-volatile Multiple-valued Circuits with Energy and Area Efficiency Using Negative Capacitance Carbon Nanotube Transistors
Hadi Akhtari
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Seyed hassan Hadi nemati
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■ Design of an Efficient True Random Number Generator Circuit based on Spintronics
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Yasin Eghlimi
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Reza Karimi
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■ Design of Low-Power and Nonvolatile FeFET-Based Logic Circuits for Computing-in-Memory Architectures
Mahsa Saeidnia
2020

■ Reduction of crosstalk effects in nano interconnects using physical parameters and crosstalk avoidance codes in multiple valued logic
Seyedeh parastoo Azimi
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Ferdos Salmanpour
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Ali Asghar Javadi
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■ Designing Energy and Area Efficient Approximate Compressors and Multipliers for Digital Signal Processing Application
Mohammad Ahmadinejad
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Mehrdad Morsali
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Kave Manafi
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Awards & Honors

■ دو درصد برتر پژوهشگران پر استناد جهان

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