



*Electrical Engineering – Electronics and
/ Telecommunications*

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Education

- Ph.D: , Electrical Engineering – Electronics

Research Interests

■

Professional Experiences

- , 1394→1397

- , 1391→1394

- , 1382→1391

Industry Collaborations

- طراحی و شبیه سازی مدولاتور سیگما-دلتا
1391

- تجهیز آزمایشگاه طراحی و پیاده سازی مبدل آنالوگ به دیجیتال جایگزین شده در زمان سیگما دلتا
1389

- طرح کلان مرکز تحقیقات مخابرات/طراحی و شبیه سازی مبدل آنالوگ به دیجیتال از نوع سیگما دلتا
1385

- اجرای طرح کلان مرکز تحقیقات مخابرات به صورت پایان نامه های دانشجویی
1384

- اجرای طرح کلان مرکز تحقیقات مخابرات به صورت پایان نامه دانشجویی
1384

■ استفاده از خدمات مطالعاتی پژوهشی آموزشی و کارشناسی دانشگاه از طریق بهره گیری از پتانسیل پژوهشی و آموزشی کشور
1382

■ FPGA بر روی آی سی ای 2x2mb/s برای استفاده در سیستمهای مخابراتی رادیو دیجیتال 4x2 طراحی و پیاده سازی ادغام 4
1380

Journal Papers

■ Mismatch error shaping of DAC unit elements in 2 multi-bit ?? modulators using a novel unified ADC/DAC

Leila Sharifi, Omid Hashemipour tafreshi

Turkish Journal of Electrical Engineering and Computer Sciences

■ A Novel Rail-to-Rail Input Swing Threshold-Inverter Multi-Bit Quantizer Using Interpolation Technique for Sampled-Data Circuits

Amir Hossein Miremadi, Omid Hashemipour tafreshi

JOURNAL OF CIRCUITS SYSTEMS AND COMPUTERS, Vol.31, 2022

■ A scalable high-linearity two-step DTC-assisted voltage-to-time converter with rail-to-rail input-range for time-based circuits

Amir Hossein Miremadi, Omid Hashemipour tafreshi

AEU-INTERNATIONAL JOURNAL OF ELECTRONICS AND COMMUNICATIONS, Vol.128, 2021

■ Digital Assisted Truncation Noise Shaping Technique for Multi-bit ?? Modulators

Leila Sharifi, Omid Hashemipour tafreshi

ARABIAN JOURNAL FOR SCIENCE AND ENGINEERING, Vol.46, pp. 1279-1286, 2021

■ Fast locking technique for phase locked loop based on phase error cancellation

Mohsen Karbalaei MohammadAli, Omid Hashemipour tafreshi

AEU-INTERNATIONAL JOURNAL OF ELECTRONICS AND COMMUNICATIONS, Vol.109, pp. 99-106, 2019

■ A simple and high performance charge pump based on the self-cascode transistor

Mohsen Karbalaei MohammadAli, Omid Hashemipour tafreshi

ANALOG INTEGRATED CIRCUITS AND SIGNAL PROCESSING, Vol.100, pp. 633-638, 2019

■ A charge sharing-based switching scheme for SAR ADCs

Meysam Akbari, Omid Hashemipour tafreshi, Masoud Nazari, Farshad Moradi

INTERNATIONAL JOURNAL OF CIRCUIT THEORY AND APPLICATIONS, Vol.47, pp. 1188-1198, 2019

■ An 8-Bit Ultra-Low-Power, Low-Voltage Current Steering DAC Utilizing a New Segment Structure

Mehdi Bandali, Alireza Hassanzadeh, Masume Ghashghai, Omid Hashemipour tafreshi

JOURNAL OF CIRCUITS SYSTEMS AND COMPUTERS, Vol.28, 2018

■ Design of active inductor-based current-controlled oscillators using gm/Id methodology

Mohammad Samiei, Omid Hashemipour tafreshi

AEU-INTERNATIONAL JOURNAL OF ELECTRONICS AND COMMUNICATIONS, Vol.87, pp. 1-9, 2018

■ Input Offset Estimation of CMOS Integrated Circuits in Weak Inversion

Meysam Akbari, Omid Hashemipour tafreshi, Farshad Moradi

IEEE TRANSACTIONS ON VERY LARGE SCALE INTEGRATION (VLSI) SYSTEMS, Vol.26, pp. 1812-1816, 2018

■ A super class-AB adaptive biasing amplifier in 65-nm CMOS technology

Meysam Akbari, Omid Hashemipour tafreshi

International Journal of Electronics Letters, Vol.6, pp. 302-314, 2018

■ A novel design of a ternary coded decimal adder/subtractor using reversible ternary gates

Mohammad Mehdi Panahi, Omid Hashemipour tafreshi, Keyvan Navi

INTEGRATION-THE VLSI JOURNAL, Vol.62, pp. 353-361, 2018

■ An energy-efficient DAC switching algorithm based on charge recycling method for SAR ADCs

Meysam Akbari, Omid Hashemipour tafreshi, Fabian Khateb, Farshad Moradi

MICROELECTRONICS JOURNAL, Vol.82, pp. 29-35, 2018

■ **Low Power Current Conveyor Based Continuous Time Sigma Delta Modulator**

Reza Chavooshi Sani, Omid Hashemipour tafreshi
Journal of Low Power Electronics, Vol.13, pp. 249-254, 2017

■ **Two-Dimensional Structure Compatible with DEM Methods Utilizing in DACs**

Mehdi Bandali, Omid Hashemipour tafreshi
JOURNAL OF CIRCUITS SYSTEMS AND COMPUTERS, Vol.27, pp. 1850142-1850155, 2017

■ **A Class-AB Bulk-Driven Amplifier with Enhanced Transconductance Using Quasi-Floating Gate Method**

Meysam Akbari, Omid Hashemipour tafreshi
JOURNAL OF CIRCUITS SYSTEMS AND COMPUTERS, Vol.27, pp. 1850137-1850146, 2017

■ **An efficient approach to enhance bulk-driven amplifiers**

Meysam Akbari, Omid Hashemipour tafreshi, Mohammad Hossein Moaiyeri, Armin Aghajani
ANALOG INTEGRATED CIRCUITS AND SIGNAL PROCESSING, Vol.92, pp. 489-499, 2017

■ **A 63-dB gain OTA operating in subthreshold with 20-nW power consumption**

Meysam Akbari, Omid Hashemipour tafreshi
INTERNATIONAL JOURNAL OF CIRCUIT THEORY AND APPLICATIONS, Vol.45, pp. 843-850, 2017

■ **Design and Analysis of an Ultra-Low-Power Second-Order Asynchronous DeltaSigma Modulator**

Meysam Akbari, Omid Hashemipour tafreshi, Farshad Moradi
CIRCUITS SYSTEMS AND SIGNAL PROCESSING, Vol.36, pp. 4919-4936, 2017

■ **Slew rate boosting technique for an upgraded transconductance amplifier**

Meysam Akbari, Alireza Hassanzadeh, Omid Hashemipour tafreshi
ANALOG INTEGRATED CIRCUITS AND SIGNAL PROCESSING, Vol.10, 2016

■ **Multi-Path Class AB Operational Amplifier with High Performance for SC Circuits**

Meysam Akbari, Omid Hashemipour tafreshi
JOURNAL OF CIRCUITS SYSTEMS AND COMPUTERS, Vol.25, 2016

■ **Ultrathin buffer layer at organic/organic interface for managing the recombination profile in organic light-emitting diodes Metal versus dielectric buffer**

Davood Kalhor, Ezeddin Mohajerani, Omid Hashemipour tafreshi, , Mohsen Shojaefar,
JOURNAL OF APPLIED POLYMER SCIENCE, Vol.133, 2016

■ **Efficient Radix-r Adders for Nanoelectronics**

Mohammad Hossein Moaiyeri, Reza Chavooshi Sani, Ali Jalali, Keyvan Navi, Omid Hashemipour tafreshi
INTERNATIONAL JOURNAL OF ELECTRONICS, Vol.103, pp. 281-296, 2016

■ **High Gain and High CMRR Two-Stage Folded Cascode OTA with Nested Miller Compensation**

Meysam Akbari, Omid Hashemipour tafreshi
JOURNAL OF CIRCUITS SYSTEMS AND COMPUTERS, Vol.24, pp. 1550057-1550080, 2015

■ **A 0.6-V 0.4- W bulk-driven operational amplifier with rail-to-rail input/output swing**

Meysam Akbari, Omid Hashemipour tafreshi
ANALOG INTEGRATED CIRCUITS AND SIGNAL PROCESSING, Vol.85, pp. 481-491, 2015

■ **High speed switched-current memory cell with very low offset and charge injection errors**

Sajad Shahsavari, Sadegh Biabanifard Hosein Abadi, Omid Hashemipour tafreshi
AEU-INTERNATIONAL JOURNAL OF ELECTRONICS AND COMMUNICATIONS, Vol.69, pp. 1627-1634, 2015

■ **DCCII based frequency compensation method for three stage amplifiers**

Mohammad amin Shahrivari, Sadegh Biabanifard Hosein Abadi, S. Mehdi Hosseini Largani, Omid Hashemipour tafreshi
AEU-INTERNATIONAL JOURNAL OF ELECTRONICS AND COMMUNICATIONS, Vol.69, pp. 176-181, 2015

■ **A High-Performance Low-Voltage Current-Mode Min/Max Circuit**

Reza Chavooshi Sani, Mohammad Hossein Moaiyeri, Omid Hashemipour tafreshi
COMPEL-THE INTERNATIONAL JOURNAL FOR COMPUTATION AND MATHEMATICS IN ELECTRICAL AND ELECTRONIC ENGINEERING, Vol.33, pp. 1172-1183, 2015

■ **Design and analysis of folded cascode OTAs using Gm/Id methodology based on flicker noise reduction**

Maisam Akbari, Omid Hashemipour tafreshi

ANALOG INTEGRATED CIRCUITS AND SIGNAL PROCESSING, Vol.83, pp. 343-352, 2015

■ **Improving power efficiency of a two-stage operational amplifier for biomedical applications**

Maisam Akbari, Masoud Nazari, Leila Sharifi, Omid Hashemipour tafreshi

ANALOG INTEGRATED CIRCUITS AND SIGNAL PROCESSING, Vol.84, pp. 173-183, 2015

■ **Systematic design of analog integrated circuits using ant colony algorithm based on noise optimization**

Meysam Akbari, Mohammad Shokouhifar, Omid Hashemipour tafreshi, Ali Jalali, Alireza Hassanzadeh

ANALOG INTEGRATED CIRCUITS AND SIGNAL PROCESSING, Vol.86, pp. 327-339, 2015

■ **An 8-Bit Unified Segmented Current-Steering Digital-to-Analog Converter**

Leila Sharifi, Masoud Nazari, Meysam Akbari, Omid Hashemipour tafreshi

ARABIAN JOURNAL FOR SCIENCE AND ENGINEERING, Vol.35, pp. 1000-1012, 2015

■ **Design of a 10-Bit High Performance Current-Steering DAC with a Novel Nested Decoder Based on Domino Logic**

Masoud Nazari, Leila Sharifi, Meysam Akbari, Omid Hashemipour tafreshi

JOURNAL OF CIRCUITS SYSTEMS AND COMPUTERS, Vol.24, pp. 1550086-1550109, 2015

■ **The effect of Indium metal nanoparticles on the electronic properties of organic light emitting diodes (OLEDs)**

Davood Kalhor, Ezeddin Mohajerani, Omid Hashemipour tafreshi

JOURNAL OF LUMINESCENCE, Vol.167, pp. 376-380, 2015

■ **A SAR-based RD modulator using shared DAC**

Shahbaz Zohr Reyhani, Omid Hashemipour tafreshi

ANALOG INTEGRATED CIRCUITS AND SIGNAL PROCESSING, Vol.79, pp. 1-10, 2014

■ **An Efficient Versatile Logic Cell for Single-Electron Technology**

, Mohammad Hossein Moaiyeri, Keyvan Navi, Omid Hashemipour tafreshi

Quantum Matter, Vol.3, pp. 57-60, 2014

■ **Enhancing transconductance of ultra-low-power two-stage folded cascode OTA**

Maisam Akbari, Omid Hashemipour tafreshi

ELECTRONICS LETTERS, Vol.50, pp. 1514-1516, 2014

■ **SAR-based delta sigma modulator using single-bit shared-DAC**

Shahbaz Zohr Reyhani, Omid Hashemipour tafreshi

ELECTRONICS LETTERS, Vol.25, pp. 23-24, 2014

■ **Efficient CNFET-based Rectifiers for Nanoelectronics**

, Keyvan Navi, Omid Hashemipour tafreshi

International Journal of Computer Applications, Vol.64, pp. 21-25, 2013

■ **CNTFET-based Designs of Energy-efficient and Symmetric 3-input Exclusive-OR and Full Adder Circuits**

, , Mohammad Hossein Moaiyeri, Keyvan Navi, Omid Hashemipour tafreshi

ARABIAN JOURNAL FOR SCIENCE AND ENGINEERING, Vol.12, pp. 3367-3382, 2013

■ **A universal method for designing low-power carbon nanotube FET-based multiple-valued logic circuits**

Mohammad Hossein Moaiyeri, , Akbar Dosetaregan, Keyvan Navi, Omid Hashemipour tafreshi

IET Computers and Digital Techniques, Vol.7, pp. 167-181, 2013

■ **Efficient Single-Electron Transistor Inverter-Based Logic Circuits and Memory Elements**

, Mohammad Hossein Moaiyeri, Keyvan Navi, Omid Hashemipour tafreshi

JOURNAL OF COMPUTATIONAL AND THEORETICAL NANOSCIENCE, Vol.10, pp. 1171-1178, 2013

■ **A High-Performance Hybrid Molecular Full Adder Cell**

Keyvan Navi, , , Omid Hashemipour tafreshi

International Review of PHYSICS (MSRT BLACKLIST), Vol.6, pp. 344-348, 2012

■ **Design and Evaluation of CNFET-based Quaternary Circuits**

Mohammad Hossein Moaiyeri, Keyvan Navi, Omid Hashemipour tafreshi

■ a new simple method for analysing of thermal noise in switched-capacitor filters

, , Omid Hashemipour tafreshi

INTERNATIONAL JOURNAL OF ELECTRONICS, pp. 1729-1737, 2012

■ A new systematic design approach for low-power analog integrated circuits

, Omid Hashemipour tafreshi, Keyvan Navi

AEU-INTERNATIONAL JOURNAL OF ELECTRONICS AND COMMUNICATIONS, pp. 384-389, 2012

■ A low-variation on resistance CMOS sampling switch for high-speed high-performance applications

Omid Hashemipour tafreshi, Mohammadreza Asgari

IEICE ELECTRONICS EXPRESS (MSRT BLACKLIST), Vol.9, pp. 339-345, 2012

■ High-performance Mixed-Mode Universal Min-Max Circuits for Nanotechnology

Mohammad Hossein Moaiyeri, Reza Chavooshisani, Ali Jalali, Keyvan Navi, Omid Hashemipour tafreshi

CIRCUITS SYSTEMS AND SIGNAL PROCESSING, Vol.31, pp. 465-488, 2012

■ Efficient CNTFET based Ternary Full Adder Cells for Nanoelectronics

Mohammad Hossein Moaiyeri, Reza Faghhi Mirzaee, Keyvan Navi, Omid Hashemipour tafreshi

Nano-Micro Letters, Vol.3, pp. 43-50, 2011

■ body effect compensation of analog awitches using variable voltage function

Omid Hashemipour tafreshi,

IEICE ELECTRONICS EXPRESS (MSRT BLACKLIST), Vol.8, 2011

■ Differential current conveyor based current comparator

Reza Chavooshisani, Omid Hashemipour tafreshi

AEU-INTERNATIONAL JOURNAL OF ELECTRONICS AND COMMUNICATIONS, pp. 949-953, 2011

■ Efficient CNTFET-based Ternary Full Adder Cells for Nanoelectronics

, Mohammad Mehdi Faghhi, Keyvan Navi, Omid Hashemipour tafreshi

JOURNAL NANO MICRO LETTERS, Vol.3, pp. 43-50, 2011

■ High Dynamic Range RNS Bases for Modular Multiplication

, , , Keyvan Navi, Omid Hashemipour tafreshi

international journal of computer science issues (IJCSI), Vol.8, pp. 69-73, 2011

■ Four Moduli RNS Bases for Efficient Design of Modular Multiplication

, , , Keyvan Navi, Omid Hashemipour tafreshi

journal of computations and modeling (JCoMod), Vol.1, pp. 73-96, 2011

■ a high speed current conveyor based current comparator

Reza Chavooshisani, Omid Hashemipour tafreshi

MICROELECTRONICS JOURNAL, Vol.42, pp. 28-32, 2011

■ A hardware-friendly arithmetic method and efficient implementations for designing digital fuzzy adders

Keyvan Navi, Akbar Dosetaregan, , Omid Hashemipour tafreshi

FUZZY SETS AND SYSTEMS, Vol.185, pp. 111-124, 2011

■ a new switched opamp approach for improving the operation of auto-reset switched-capacitor filters

Mohammad Rashtian, Omid Hashemipour tafreshi, Keyvan Navi, Ali Jalali

JOURNAL OF CIRCUITS SYSTEMS AND COMPUTERS, Vol.20, pp. 835-848, 2011

■ high speed capacitor -inverter based carbon nanotube full adder

Keyvan Navi, , , Omid Hashemipour tafreshi

Nanoscale Research Letters, Vol.5, pp. 859-862, 2010

■ a simple time domain approach to noise anaalysis of switched capacitor circuits

Omid Hashemipour tafreshi, Mohammad Rashtian, Afshin Hemmatyar

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■ five new mvl current mode differential absolute value circuits based on carbon nano-tube field effect transistors

(cntfets)

, Fazel Sharifi rostam abadi, Keyvan Navi, Omid Hashemipour tafreshi
JOURNAL NANO MICRO LETTERS, Vol.2, 2010

■ **A NOVEL STRUCTURE FOR REALIZATION OF A PSEUDO TWO PATH BAND-PASS FILTER**

, Omid Hashemipour tafreshi, Keyvan Navi, Ali Jalali
International Journal of Engineering, Vol.23, pp. 201-208, 2010

■ **an energy efficient full adder cell for low voltage**

Keyvan Navi, mehrdad maeen, Omid Hashemipour tafreshi
IEICE TRANSACTIONS ON FUNDAMENTALS OF ELECTRONICS COMMUNICATIONS AND COMPUTER SCIENCES, Vol.6, pp. 553-559, 2009

■ **Two new low-power full adders based on majority-not gates**

Keyvan Navi, , Omid Hashemipour tafreshi, Babak Mazloomnejad Meybodi
MICROELECTRONICS JOURNAL, Vol.40, pp. 126-130, 2009

■ **A low voltage bootstrapped switch based on zero DC offset input voltage**

Alihosein Sepahvand, Omid Hashemipour tafreshi
IEICE ELECTRONICS EXPRESS (MSRT BLACKLIST), Vol.5, pp. 932-935, 2008

■ **An efficient architecture for designing reverse converters based on a general**

Keyvan Navi, , Omid Hashemipour tafreshi, Ali Jalali
JOURNAL OF SYSTEMS ARCHITECTURE, Vol.54, pp. 929-934, 2008

■ **An efficient architecture for designing reverse converters based on a general three-moduli set**

, Keyvan Navi, Omid Hashemipour tafreshi, Ali Jalali
JOURNAL OF SYSTEMS ARCHITECTURE, Vol.54, pp. 929-934, 2008

■ **A six transistors full adder**

Keyvan Navi, , Babak Mazloomnejad Meybodi, , Omid Hashemipour tafreshi
WORLD APPLIED SCIENCES JOURNAL, Vol.4, pp. 142-149, 2008

■ **Ultra high speed full adders**

Keyvan Navi, , Masoud Moayeri, Babak Mazloomnejad Meybodi, Omid Hashemipour tafreshi
IEICE ELECTRONICS EXPRESS (MSRT BLACKLIST), Vol.5, pp. 744-749, 2008

■ **Design of a novel reversible multiplier circuit using HNG gate in nonotechnology**

Majid Haghparast, , Omid Hashemipour tafreshi, Keyvan Navi
WORLD APPLIED SCIENCES JOURNAL, Vol.3, pp. 974-978, 2008

■ **A New Hybrid Brushless dc Motor / Generator without Permanent Magnet**

Seyed ebrahim Afjei, Omid Hashemipour tafreshi, M.A Saati, M.M nezamabadi
International Journal of Engineering, Vol.20, pp. 77-86, 2007

■ **A Self-Tunable Sensorless Method for Rotor Position Detection in Switched Reluctance Motor Drive**

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Iranian Journal of Science and Technology Transaction B-Engineering, Vol.31, pp. 317-328, 2007

■ **High Speed Full Swing Current Mode Bicmos Logical Operators**

Keyvan Navi, , Omid Hashemipour tafreshi
International journal of electronics Transaction A Basics, Vol.20, 2007

■ **A 1.5V 100MS/s 12-bit Current-mode CMOS Sample-and-Hold Circuit**

Omid Hashemipour tafreshi,
WORLD ENFORMATIKA SOCIETY, Vol.4, pp. 128-131, 2006

■ **A very low voltage 9th order linear phase Baseband Switched Capacitor filter**

Omid Hashemipour tafreshi
International Journal of Engineering, Vol.17, pp. 25-28, 2004

■ **Dopant Deactivation and annealing characteristics of MOS structure on Ge/B doped Si after Gamma irradiation or Fowler-Nordheim charge injection**

Omid Hashemipour tafreshi, s.s Ang

■ Metal-Oxide-Semiconductor structure on germanium/boron doped silicon

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■ طراحی یک فیلتر پایین گذر چبی شف درجه چهارم سوییچ خازنی با ساختار انتگرال گیر خود صفر شونده در ولتاژ تغذیه ۲/۱ ولت

امید هاشمی پور تفرشی، محمد رشتیان، کیوان ناوی

مهندسی برق و مهندسی کامپیوتر ایران، نسخه ۳، صفحات: ۱۷۸-۱۸۲، ۱۳۸۵

Conference Papers

■ multi bit quantizer delta-sigma modulator with the feedback dac mismatch error shaping

Leila Sharifi, Omid Hashemipour tafreshi

27th Iranian Conference on Electrical Engineering ICEE2019

■ A High Slew Rate CMOS OTA with Dynamic Current Boosting Paths

Meysam Akbari, Omid Hashemipour tafreshi, farshad moradi

2018 IEEE International Symposium on Circuits and Systems (ISCAS)

■ Dramatically Low-Transistor-Count High-Speed Ternary Adders

, Mohammad Hossein Moaiyeri, , Keyvan Navi, Omid Hashemipour tafreshi

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■ On the design of new low-power CMOS standard ternary logic gates

Akbar Dosetaregan, Mohammad Hossein Moaiyeri, Keyvan Navi, Omid Hashemipour tafreshi

CSI Internationa Symposium on Computer Architecture and Digital Systems(CADS), pp.115-120

■ A novel structure for realization of a pseudo two path band-pass filter

, Omid Hashemipour tafreshi, Keyvan Navi

2008 26th international conference on microelectronics

■ A New Hybrid Reluctance Motor/Field-Assisted Generator

Seyed ebrahim Afjei, Omid Hashemipour tafreshi,

IEEE International Electric Machines Drives Conference 2007. IEMDC 07, pp.543-547

■ A high precision cmos bandgap reference based on curvature correction circuit

میثم اکبری، امید هاشمی پور تفرشی

سومین همایش بین المللی مهندسی برق، علوم کامپیوتر و فناوری اطلاعات، صفحات: ۴-۸

■ A ۱۲-bit high performance current-steering DAC using a new binary to thermometer decoder

مسعود نظری، لیلا شریفی، آرمین آقاجانی، امید هاشمی پور تفرشی

بیست و چهارمین کنفرانس مهندسی برق ایران، صفحات: ۹۱۹-۹۲۴

■ Design of a new split-capacitive-array based on distribution of attenuation capacitor

مسعود نظری، آرمین آقاجانی، امید هاشمی پور تفرشی

بیست و سومین کنفرانس مهندسی برق ایران، صفحات: ۱۳۷۰-۱۳۷۳

■ An ultra-low voltage ultra-low power fully recycling folded cascode amplifier

میثم اکبری، امید هاشمی پور تفرشی، اردوان جاوید

صفحات: ۵۱۴-۵۱۸، ۲۰۱۴ IEEE بیست و دومین کنفرانس مهندسی برق ایران

■ Design of Ultra-Low-Power CMOS Amplifiers Based on Flicker Noise Reduction

میثم اکبری، صادق بیابانی فردحسین آبادی، امید هاشمی پورتفرشی
صفحات: ۴۰۳-۴۰۶، ۲۰۱۴، ICEE
دومین کنفرانس مهندسی برق ایران

■ A reliable full-swing low-distortion cmos bootstrapped sampling switch

محمدرضا عسگری، سید حسین پیشگرکومله، امید هاشمی پورتفرشی
صفحات: ۲۰۰-۲۰۳، (icecs)، ۲۰۰۰، IEEE international conference on electronics circuits and systems

■ یک تقویت کننده عملیاتی جدید با پهنای باند وسیع با فیدبک جریان

رضا ملکی قره بلاغ، امید هاشمی پورتفرشی
ICEE ۲۰۰۸ شانزدهمین کنفرانس مهندسی برق

■ طراحی یک نمونه بردار و نگهدارنده دقیق به کمک تقویت کننده سوئیچ شونده

محمد رشتیان، امید هاشمی پورتفرشی، کیوان ناوی
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■ معکوس کننده های سه مقداری بسیار سریع

کیوان ناوی، امید هاشمی پورتفرشی، سعید گرگین
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■ معرفی یک ساختار مناسب برای مدارات سوئیچ خازنی سریع در کارکرد های ولتاژ پایین

امید هاشمی پورتفرشی، محمد رشتیان
کنفرانس بین المللی مهندسی برق ایران

thesis and doctoral thesis

■
Amir Hossein Miremadi
2022

■
Leila Sharifi
2020

■
Mohsen Karbalaeei MohammadAli
2020

■
Meysam Akbari
2019

■
Reza Chavooshi Sani
2017

■
Shahbaz Zohr Reyhani

2014

■

2013

■

2012

■

2012

■

Babak Mazloomnejad Meybodi

2009

M.Sc. Theses

■ Optimization Of Folded Cascode Based On Evolutionary Algorithms

Mahshidalsadat Marashi

2021

■ Sensorless Control of High Power PM BLDC Motors from Standstill to High Speed

Mohammad Ranjbar

2021

■ Design and Simulation of an ultra Low Power Amplifier for low-Frequency application in Sub-Threshold region

Masoud Heydari

2021

■ Gain and Slew Rate Enhancement of Operational Transconductance Amplifier With Low Frequency Applications

Seyed Arash Katourani

2021

■ performance improvement of a high frequency variable gain amplifier

Shaqayeq Saljooqi Badloo

2020

■ A Low power and high-speed comparator

Rayoumand Yazdani

2020

■ matching improvement of current sources in charge pump

Farzaneh Radpour

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■ Design of hybrid OTA for large capacitive loads

Seyyed mohsen Beheshti

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■ Design of a low voltage hybrid OTA for large capacitive loads

Mohsen Abdollahipoor

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■ Improving the linearity of gm-cell
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